

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Kelvin Ma, et al.	)	Docket No.:	TA-00633
		)		
Serial No.:	TBD	)	Examiner:	TBD
		)		
Filed:	Herewith	)	Art Unit:	TBD
		)		
For:	Optical Interconnect and Method	)		
	For Making the Same	)		

## INFORMATION DISCLOSURE STATEMENT

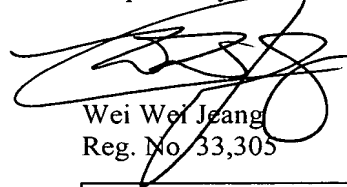
Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Box: Patent Application

Dear Sir:

Applicants enclose the attached PTO-1449 form citing references to be considered and cited in the examination for above-identified patent application filed herewith. Copies of the references are enclosed for the convenience of the Examiner.

Applicants believe no fee is due. However, the Director is authorized to charge any fees or credit any overpayments to Deposit Account 13-4900.

Respectfully submitted,

  
Wei Wei Jeang  
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Date: Aug. 26, 2003  
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Vikki R. Colvin  
Vikki R. Colvin

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<b>Substitute for form 1449B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>		<b>Complete if Known</b>			
		Application Number	TBD		
		Filing Date	August 26, 2003		
		First Named Inventor	Kelvin Ma, et al.		
		Group Art Unit	TBD		
		Examiner Name	TBD		
Sheet	1	of	1	Attorney Docket Number	TA-00633

<b>OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	A	KIRK, A.G., et al., "A Multistage Free-Space Optical Interconnect for Backplane Applications: Implementation Issues," April, 1998, Institute of Electrical and Electronic Engineers, pgs. 149-150.	
	B	ROBERTSON, Brian, "Design of an Optical Interconnect for Photonic Backplane Applications," Applied Optics, Vol. 37, No. 14, May 10, 1998, pgs. 2974-2984.	
	C	World Wide Web, <a href="http://www.oe.uni-duisburg.de/Reports/jb95/mod1.html">http://www.oe.uni-duisburg.de/Reports/jb95/mod1.html</a> , HEINZELMANN, R., "Electro-optic Modulators for Hybrid Integration," printed on April 23, 2003.	
	D	World Wide Web, <a href="http://www.ececs.uc.edu/~psdl/projects/WaveguideTaps/WaveguideTaps.htm">http://www.ececs.uc.edu/~psdl/projects/WaveguideTaps/WaveguideTaps.htm</a> , "Optical Waveguide Taps on Silicon CMOS Circuits," STENGER, Vincent et al., printed 04/23/03.	
	E	BOGALECKI, Alfons W., "Design of a Waveguide Based Optoelectronic Integrated Circuit in Silicon 0.8 μm BiCMOS Technology," 2000, 4 pgs.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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